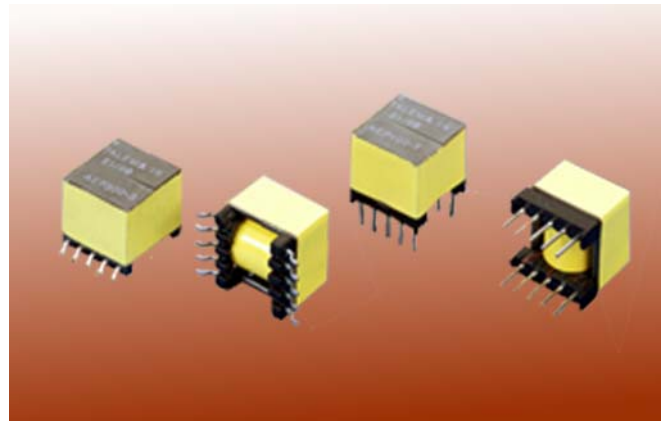




ADSL Transformers for Centillium Chipsets

Features

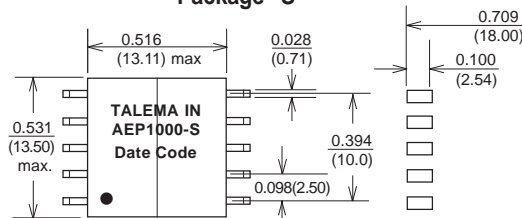
- complies with ANSI and ETSI requirements
- matched to Centillium CT-L21SC08, CT-L41SC04, CT-L50SC04, CT-L50ST81 & CR-L10AT01-1T chipsets
- very low THD
- 1500V minimum isolation
- all materials approved to UL-94V-0
- excellent quality at competitive price due to high volume production
- manufactured in ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- fully RoHS compliant and meets lead free reflow level J-STD-020C



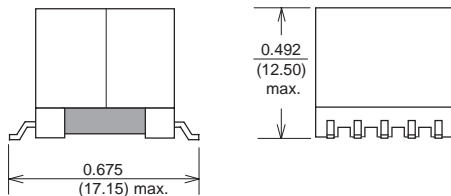
Electrical Specifications @ 25°C • Operating Temperature -40° to +85°C

Part Number	Package	IC Driver	Turns Ratio ±2% Line : IC	L _p (µH)	L _L (µH) Max	C _{ww} (pF) Max	DCR (Ohms) Max Line / IC	THD (Linearity) Typical	Longitudinal Balance Min	Insertion Loss Max	Freq. Response Max	Isolation Voltage (Vrms)
AEP1000-T AEP1000-S	TH SMD	CT-L21SC08 CO CT-L41SC04 CO	1 : 1	462 ±10%	5	60	1.30 / 1.30	-67dB 10kHz	-50dB 10kHz - 1.1MHz	0.5dB 100kHz	1.0dB 30kHz-1.1MHz	1500
AEP1010-T AEP1010-S	TH SMD	CT-L50SC04 CO/CPE CT-L50ST81 CO/CPE	1.8 : 1	462 ±10%	10	70	1.30 / 0.90	-67dB 10kHz	-50dB 10kHz - 1.1MHz	0.5dB 100kHz	1.0dB 30kHz-1.1MHz	1500
AEP1020-S	SMD	CT-L10AT01-1T	1 : 1.1	364 ±5%	11	60	2.10 / 2.20	-67dB 10kHz	-45dB 20kHz - 1.1MHz	1.5dB 50kHz	1.0dB 40kHz-1.1MHz	1500

Package "S"



Note: P/N AEP1020-S will be supplied without pin #3

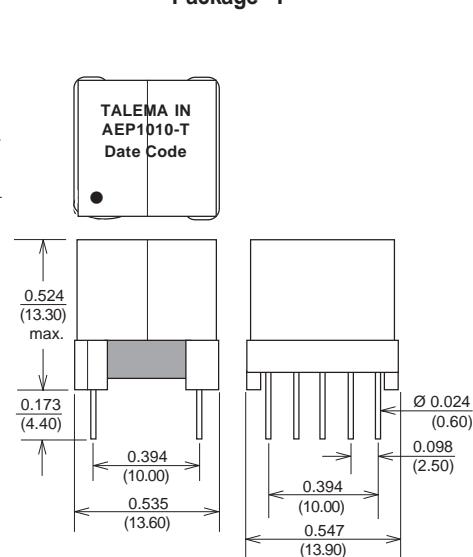


Dimensions: Inches(Millimeters)

Tolerance: ±0.010 (0.25)

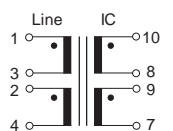
Weight: 6.8 grams

Package "T"

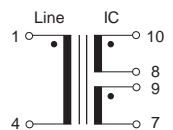


Schematic

AEP1000-S & T AEP1010-S & T



AEP-1020-S



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