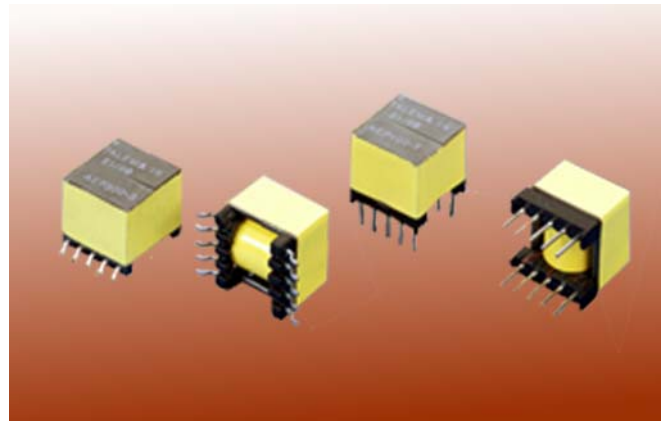




SDSL/SHDSL Transformers for Infineon Chipsets

Features

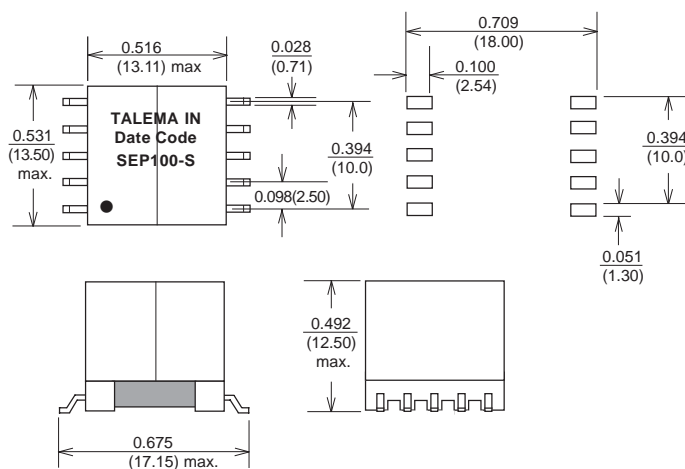
- complies with ANSI and ETSI requirements
- matched to Infineons PEB22622, PEF22623 & PEF24622 chipsets
- very low THD
- 2000V minimum isolation
- all materials approved to UL94V-0
- excellent quality at competitive price due to high volume production
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 approved Talema facility
- fully RoHS compliant and meets lead free reflow level J-STD-020C



Electrical Specifications @ 25°C • Operating Temperature -40° to +85°C

Part Number	Package	Turns Ratio ±2% Line : IC : Sense	L _p (mH) ±10%	L _L (µH) Max.	DCR (Ohms Max.) Line / IC	DCR (Ohms Max.) Sense Winding	Isolation Voltage (Vrms)
SEP100-T	TH	3.2 : 1 : 1	3.0	25	5.0 / 0.80	3.6	2000
SEP100-S	SMD	3.2 : 1 : 1	3.0	25	5.0 / 0.80	3.6	2000

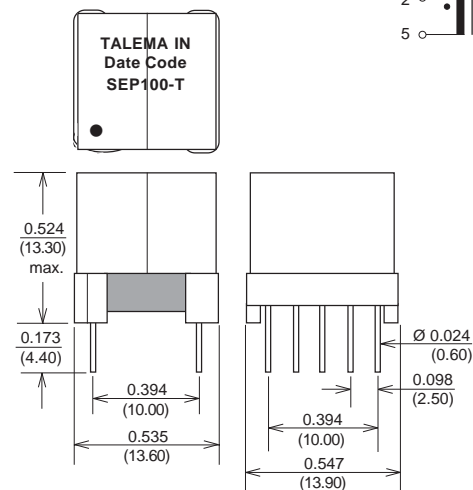
Package "S"



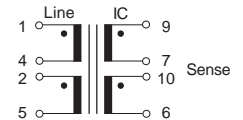
Dimensions: Inches (Millimeters)
Tolerance: ±0.010 (0.25)

Weight: 6.8 grams

Package "T"



Schematic



Sales & Marketing, Design and Manufacturing Facilities

<http://www.talema-nuvotem.com>

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