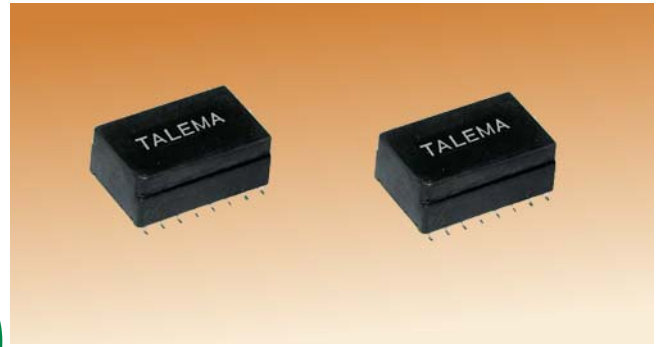


ISDN • U_{PO} SMD Interface Transformer Modules

Features

- matched to Infineon's PEB2096, PSB2196 & PSB2197 chipsets
- excellent and consistent balance between windings
- compact size in surface mount package
- complies fully with all international standards for U-Interface
- manufactured in an ISO-9001:2001, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- operating temperature -40° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C



Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

Surface Mount U_{PO} Interface Transformer Modules Complies with Basic Insulation Level EN60950, UL1950 and UL1459

Part Number	Turns Ratio ±2%	L _P (mH min)	I _{DC} (mA)	L _L (µH)	C _C (pF Max)	DCR (Ohms per winding)		V _P (Vrms)	Schematic
						Pri	Sec		
UAJ-140C	1:1:2:2	1.7	75	5	150	0.8	1.6	1500	C

Test Conditions:

Inductance: Line side windings in series - measurement @ 10kHz, 100mVrms

Leakage Inductance: Line side windings in series, IC side winding/s short circuited - measurement @ 100kHz, 100mVrms

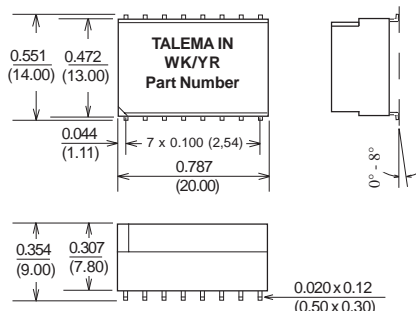
Coupling Capacitance: IC side winding (windings in series) to Line side windings in series @ 10kHz, 100mVrms

Test Voltage: 1.5kV for 2 sec. - w1+w2 to w3+w4

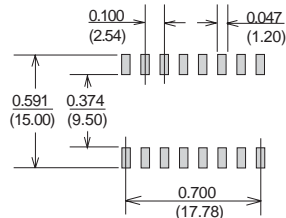
0.5kV for 2 sec. - w1+w3 to w2+w4

High Voltage Test: U_{PULSE}(w1+w2 to w3+w4); 2kV_{OP} for 10µs/700µs wave form, 10 pulses in 10 second cycle with changing polarity.

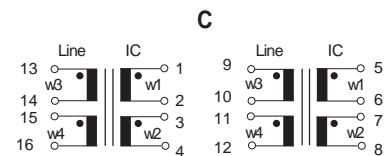
Dimensions



Suggested Pad Layout



Schematic



Surface Coplanarity will be 0.004(0.10)

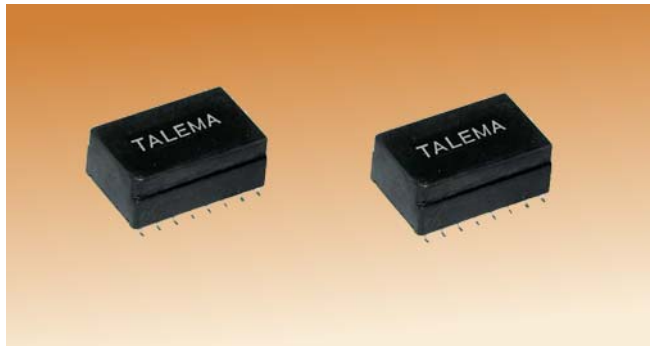
Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise



Features

- designed for optimum compatibility with all established interface IC's
- excellent and consistent balance between windings
- compact size in surface mount package
- complies fully with all international standards for U-Interface
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- operating temperature -40° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C



Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

Surface Mount U_{PO} Interface Transformer Modules
Complies with Basic Insulation Level EN60950, UL1950 and UL1459

Part Number	Turns Ratio ±2%	L _p (mH min)	I _{DC} (mA)	L _L (µH)	C _C (pF Max)	DCR (Ohms per winding)		V _P (Vrms)	Schematic	Double Choke	
						Pri	Sec			L _N (mH)	DCR (Ohms)
UAJ-120A-502	1:1: 2	1.7	75	5	100	0.8	1.6	1500	A	5	0.4
UAJ-130A-502	1:1: 2,5	1.7	75	5	100	0.5	1.0	1500	A	5	0.4
UAJ-140B-502	1:1: 2:2	1.7	75	5	180	0.8	1.6	1500	B	5	0.4

Test Conditions:

Inductance: Line side windings in series - measurement @ 10kHz, 100mVrms

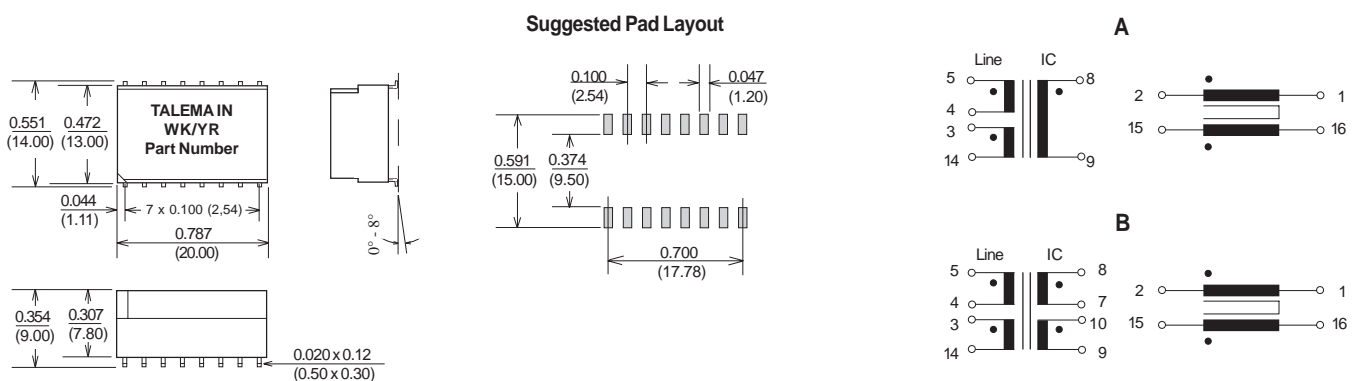
Leakage Inductance: Line side windings in series, IC side winding/s short circuited - measurement @ 100kHz, 100mVrms

Coupling Capacitance: IC side winding (windings in series) to Line side windings in series @ 10kHz, 100mVrms

Test Voltage: 1.5kV for 2 Sec. - Line side windings in series to IC side winding (IC windings in series)

Inductance CM Choke: Measured @ 10kHz, 100mVrms

Dimensions & Schematic



Surface Coplanarity will be 0.004(0.10) maximum

Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise