

ISDN - U_{PO} SMD Single Interface Transformers

Features

- matched to Infineon's PSB2196 & PSB2197 chipsets
- excellent and consistent balance between windings
- complies fully with all international standards for U-Interface
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- operating temperature: -40° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C



Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

Surface Mount U_{PO} Interface Transformers Complies with Reinforced Insulation Level EN60950, UL1950 and UL1459

Part Number	Turns Ratio ±2%	L _P (mH min)	L _L (μH)	I _{DC} (mA)	C _C (pF Max)	DCR (Ohms)		V _P (Vrms)
						Primary	Secondary	
UHJ-185	1:1:2:2	1.7	20	120	20	1.3	1.3	3000

Test Conditions:

Inductance: Line side windings in series - measurement @ 10kHz, 100mVrms

Leakage Inductance: Line side windings in series, IC side winding short circuited - measurement @ 100kHz, 100mVrms

Coupling Capacitance: IC side winding in series to Line side windings in series @ 10kHz, 100mVrms

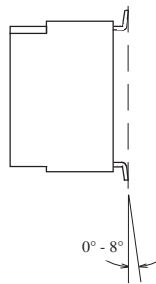
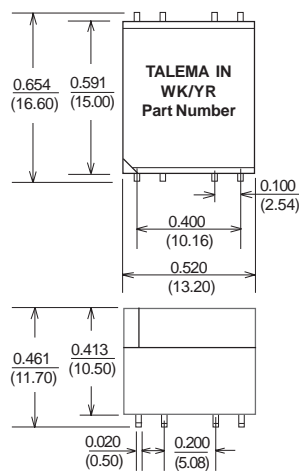
Test Voltage: 3kV for 2 sec. Line side windings to IC side windings

Reinforced Insulation: Line Side in series to IC side in series

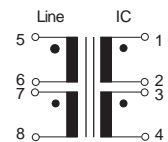
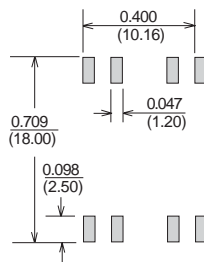
Working Voltage: 250Vrms

Dimensions & Schematic

UHJ



Suggested Pad Layout



Surface Coplanarity will be 0.004(0.10) maximum

Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise

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