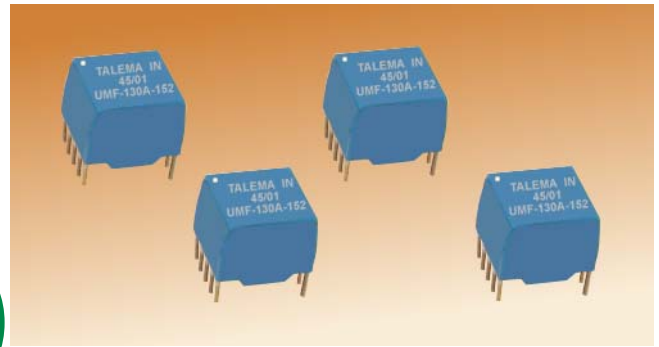


ISDN • U_{PO} THT Interface Transformer Module

Features

- matched to Infineon's PEB2095 IBS and AMD's AM2095 & AM20950 chipsets
- excellent and consistent balance between windings
- compact size in through-hole package
- complies fully with all international standards for U-Interface
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- operating temperature -40° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C



Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

U_{PO} TH Interface Transformer Module Complies with Basic Insulation Level EN60950, UL1950 and UL1459

Part Number	Turns Ratio ±1%	L _P (mH Min)	I _{DC} (mA)	L _L (µH)	C _C (pF Max)	DCR (Ohms per winding)		V _P (Vrms)	Schematic	Double Choke	
						Pri	Sec			LN (mH)	DCR (Ohms)
UMF-130A-152	1:1: 2,5	1,0	75	6	100	0,31	0,65	1500	A	1,5	0,36

Test Conditions:

Inductance: Line side windings in series - measurement @ 10kHz, 100mVrms

Leakage Inductance: Line side windings in series, IC side windings short circuited - measurement @ 100kHz, 100mVrms

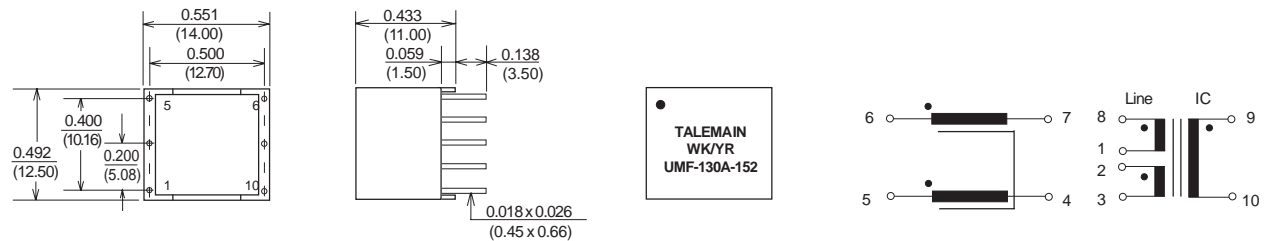
Coupling Capacitance: IC side winding to Line side windings in parallel @ 10kHz, 100mVrms

Test Voltage: Transformer - 1,5kV for 1 sec. - Line side windings in series to IC winding

Choke - 0,5kV for 1 sec. - winding to winding

Dimensions & Schematic

UMF



Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise

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