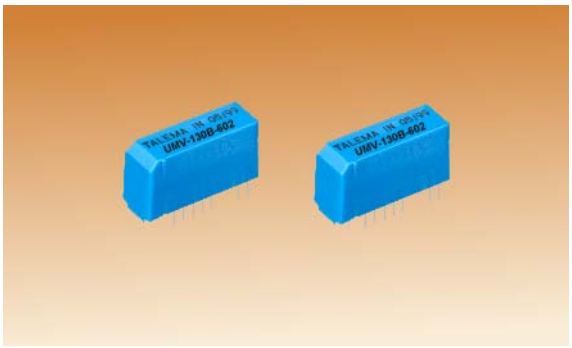
ISDN • U_{PO} THT Interface Transformer Modules



Features

- matched to Infineon's PEB2095 IBS and AMD's AM2095 & AM20950 chipsets
- excellent and consistent balance between windings
- compact size in through-hole package
- complies fully with all international standards for U-Interface
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- operating temperature -40° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C

Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

U_{PO} TH Interface Transformer Modules
Complies with Basic Insulation Level EN60950, UL1950 and UL1459

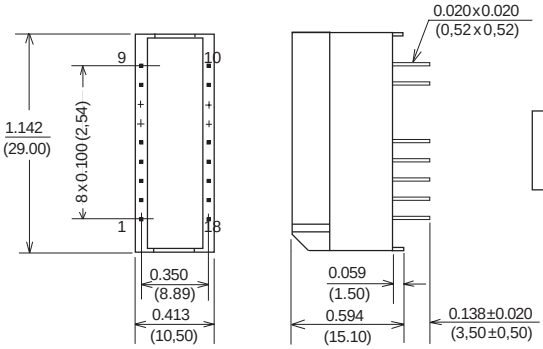
Part Number	Turns Ratio ±1%	L_P (mH Min)	$\hat{}I_{DC}$ (mA)	L_L (µH)	C_C (pF Max)	DCR (Ohms per winding)		V_P (Vrms)	Schematic	Double Choke	
						Pri	Sec			LN (mH)	DCR (Ohms)
UMV-130B-172	1:1:**2,5**	1,7	75	6	100	0,80	2,00	2500	B	1,7	0,25
UMV-130B-602	1:1:**2,5**	1.7	75	6	100	0,80	2,00	2500	B	6,0	0,80

Test Conditions:

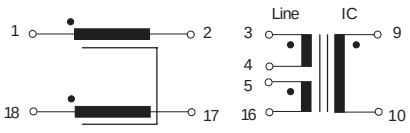
Inductance: Line side windings in series - measurement @ 10kHz, 100mVrms
Leakage Inductance: Line side windings in series, IC side windings short circuited - measurement @ 100kHz, 100mVrms
Coupling Capacitance: IC side winding to Line side windings in parallel @ 10kHz, 100mVrms
Test Voltage: Transformer - 2,5kV for 1 sec. - Line side windings in series to IC winding
0,5kV for 1 sec. - Primary winding to Primary winding
Choke - 0,5kV for 1 sec. - winding to winding

Dimensions & Schematic

UMV



TALEMAINWK/YR
UMV-130B-XXX



Dimensions: Inches (Millimeters)
Tolerance: ±0.010 (0.25) unless specified otherwise