



# ISDN - U<sub>PO</sub> SMD Single Interface Transformers

## Features

- matched to Infineon's PEB20950 & PEB20951 and National's TP34 & TP3406 chipsets
- excellent and consistent balance between windings
- complies fully with all international standards for U-Interface
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- operating temperature: -40° to +85°C
- fully RoHS compliant and meets lead free reflow level J-STD-020C



## Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

U <sub>PO</sub> SMD Interface Transformers								
Complies with Basic Insulation Level EN60950, UL1950 and UL1459								
Part Number	Turns Ratio ±2%	L <sub>P</sub> (mH min)	L <sub>L</sub> (µH)	I <sub>DC</sub> (mA)	C <sub>C</sub> (pF Max)	DCR (Ohms)		V <sub>P</sub> (Vrms)
						Primary	Secondary	
UWJ-180	<b>1:1:4</b>	1.7	10	140	100	1.5	5.2	1500
UWJ-185	<b>1:1:2</b>	1.7	20	140	90	1.5	3.0	1500

## Test Conditions:

Inductance: Line side windings in series - measurement @ 10kHz, 100mVrms

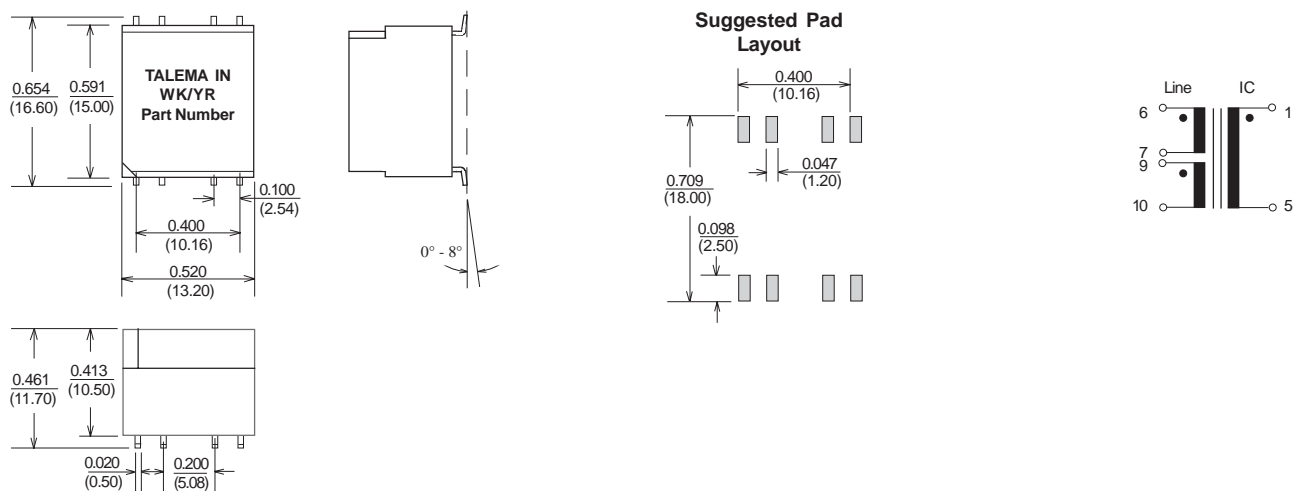
Leakage Inductance: Line side windings in series, IC side winding short circuited - measurement @ 100kHz, 100mVrms

Coupling Capacitance: IC side winding to Line side windings in series @ 10kHz, 100mVrms

Test Voltage: 1500 V for 2 Sec. Line side windings in series to IC side winding

## Dimensions & Schematic

### UWJ



Surface Coplanarity will be 0.004(0.10)

Dimensions: Inches (Millimeters)

Tolerance: ±0.010 (0.25) unless specified otherwise